

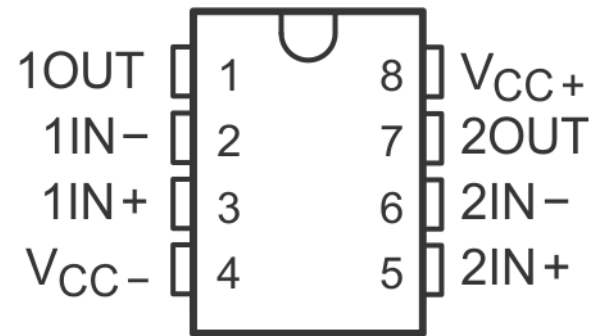
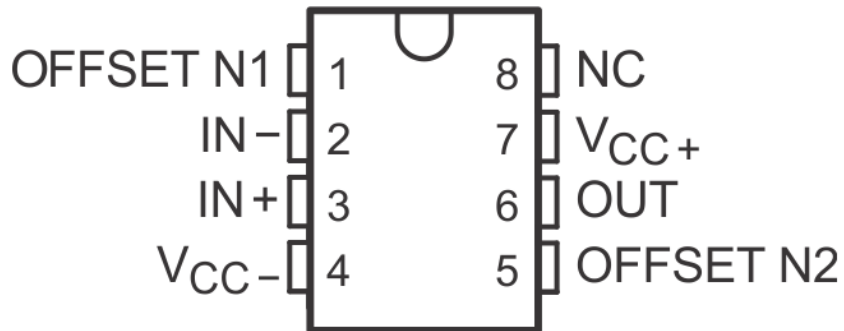
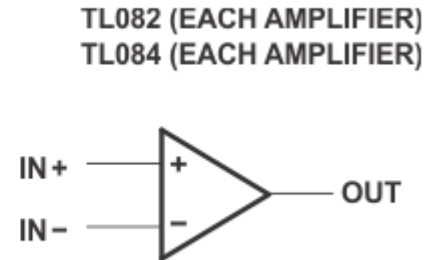
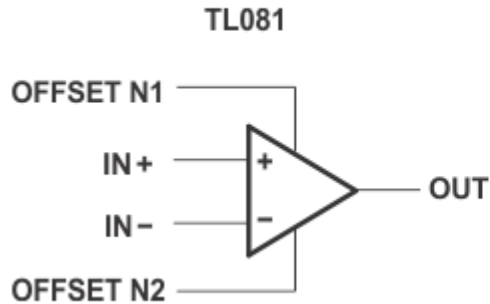
Operational Amplifier

Operational Amplifier

Invented Karl D. Swartzel Jr.
 First production 1941

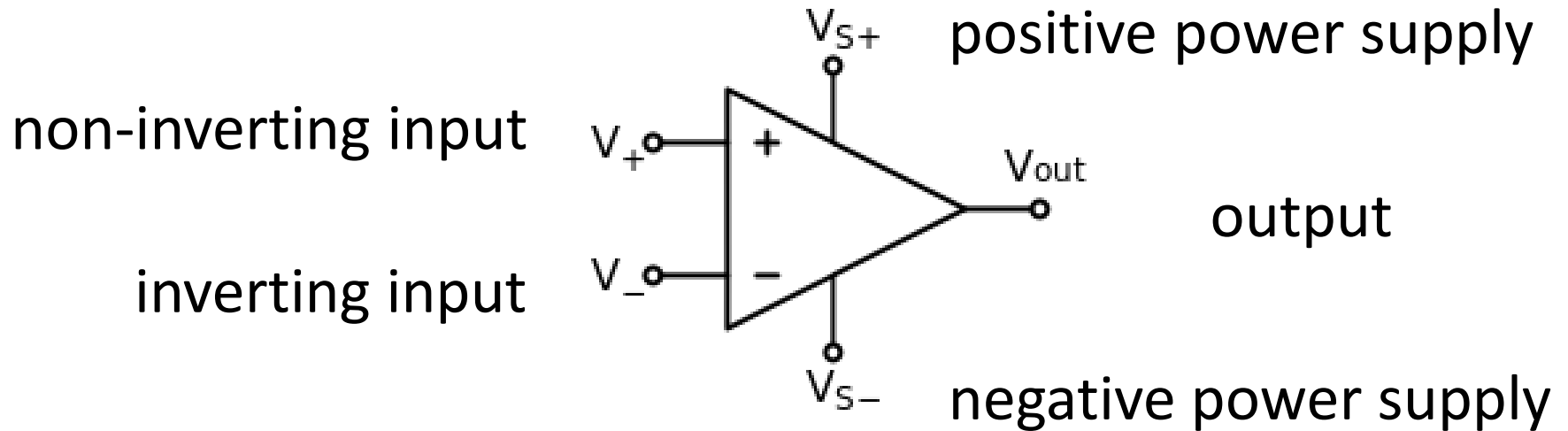


Schematic Symbol

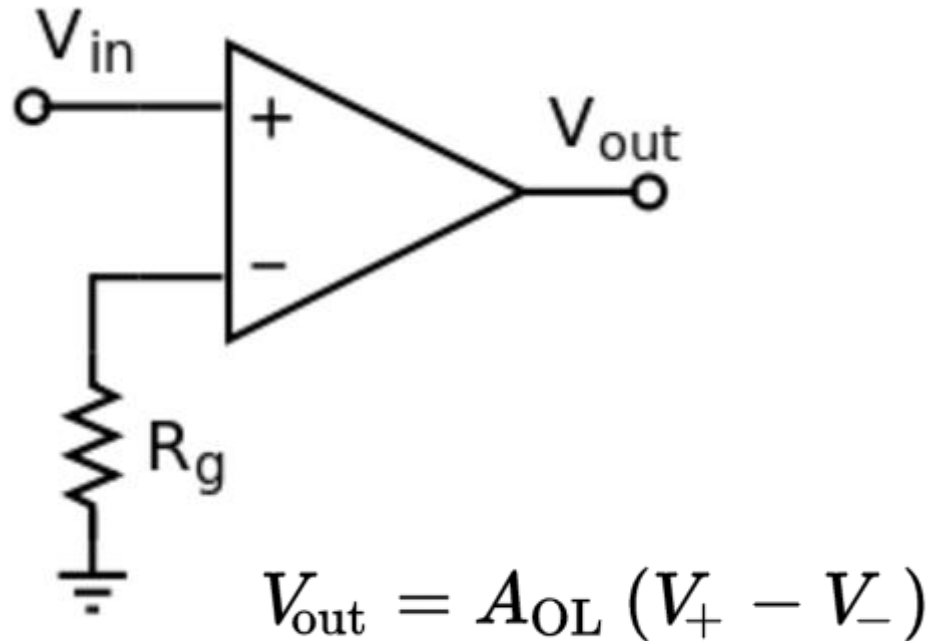


Symbol for Operational Amplifier

Pin configuration



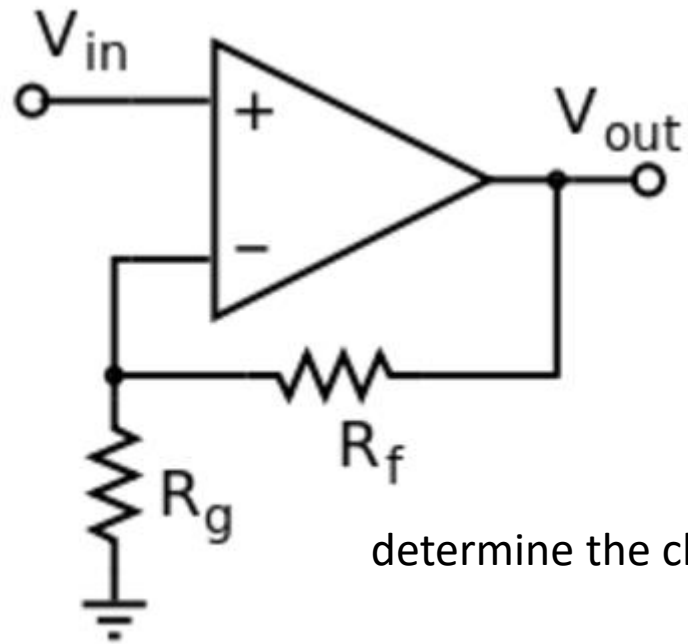
Open loop amplifier



The magnitude of A_{OL} is typically very large—100,000 or more

Closed loop amplifier

An op-amp with negative feedback (a non-inverting amplifier)



determine the closed-loop gain A_{CL} :

$$A_{CL} = \frac{V_{out}}{V_{in}} = 1 + \frac{R_f}{R_g}$$

Operational Amplifier in Closed-loop

R_f = Feedback resistance because it is connected in the feedback path of the circuit (connecting input and output points).

R_1 = Resistance connected in the input side of the circuit.

$$A_{VCL} = A_v = \frac{V_o}{V_i} = \left(\frac{R_f}{R_1} \right)$$

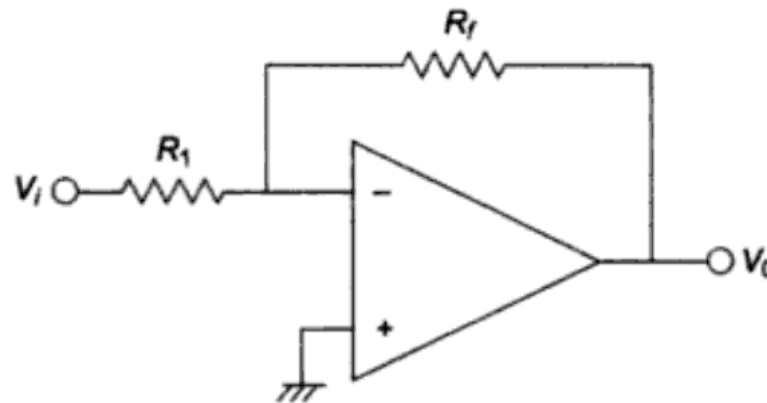
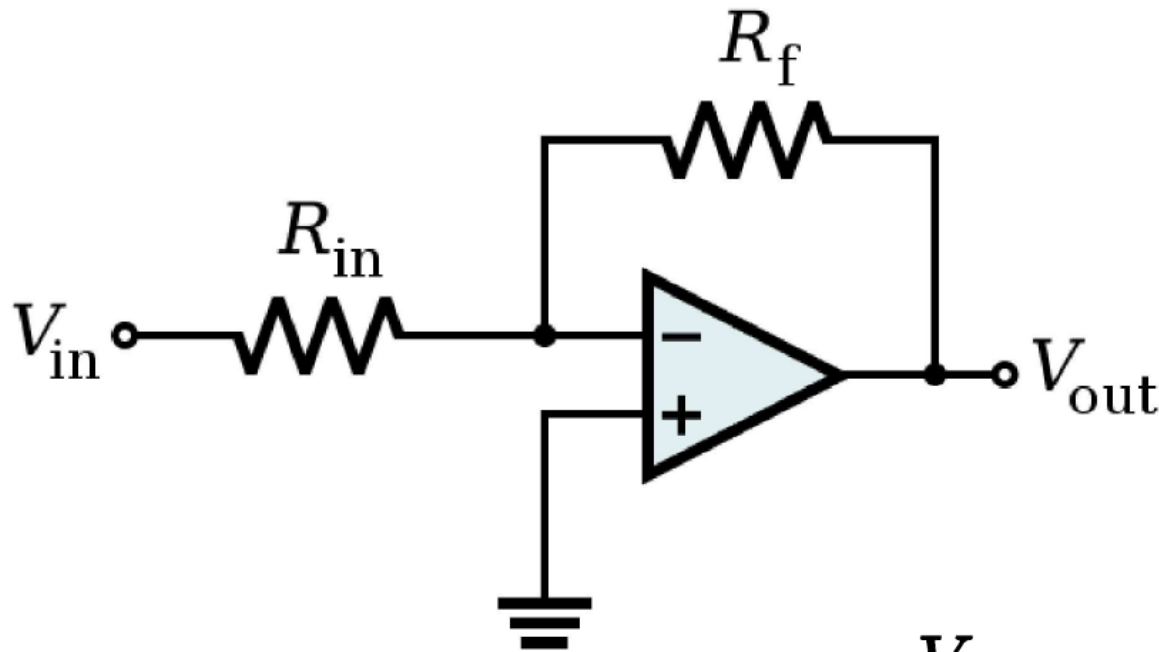


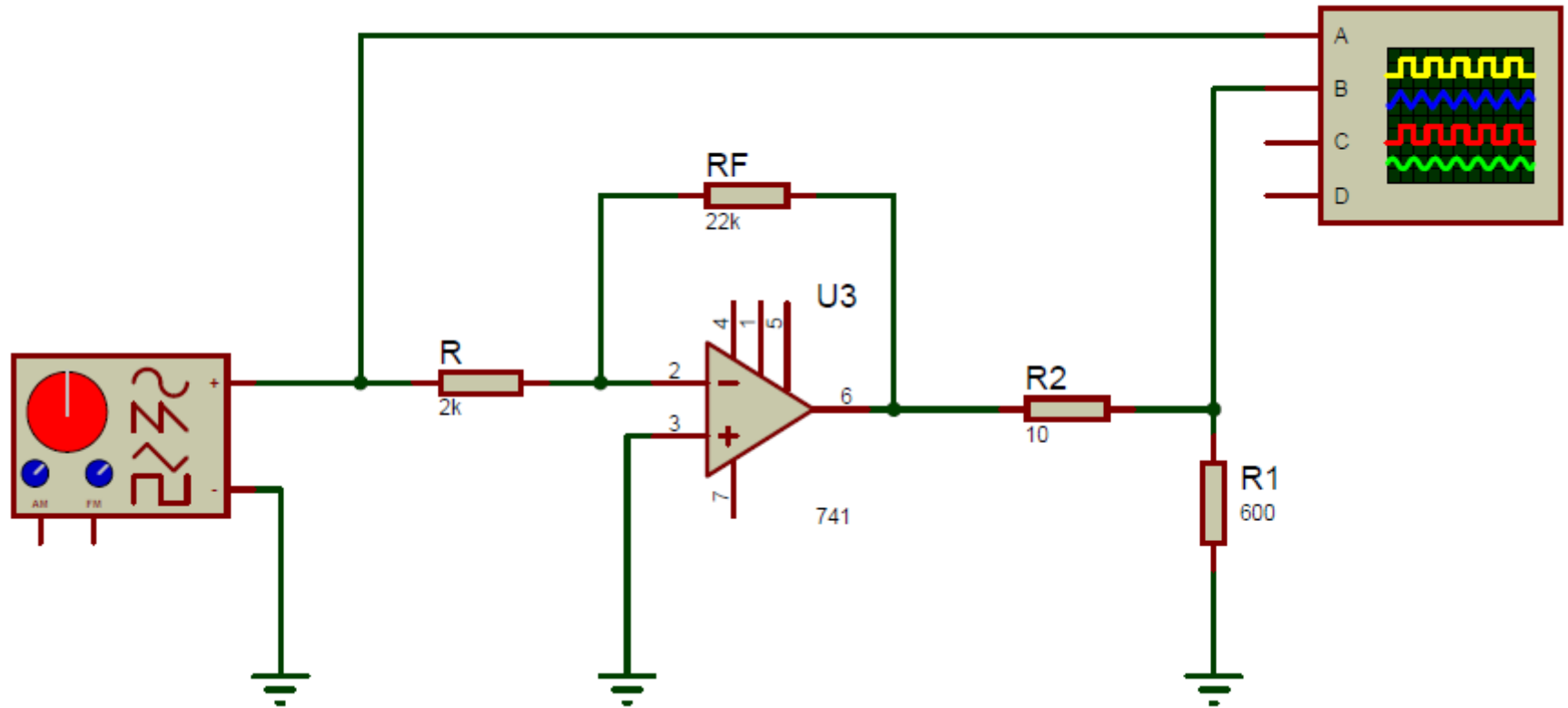
Fig. 1.13 Operational Amplifier in Closed-loop

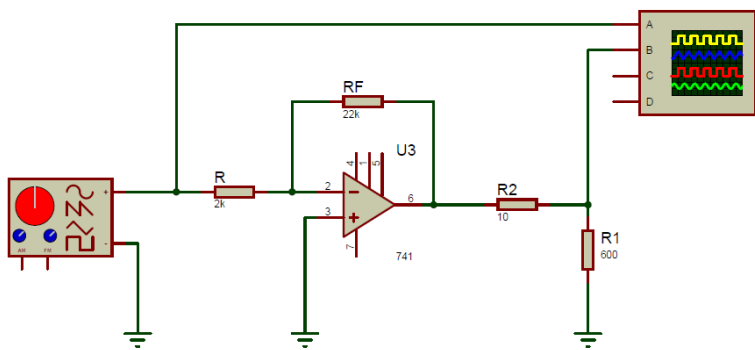
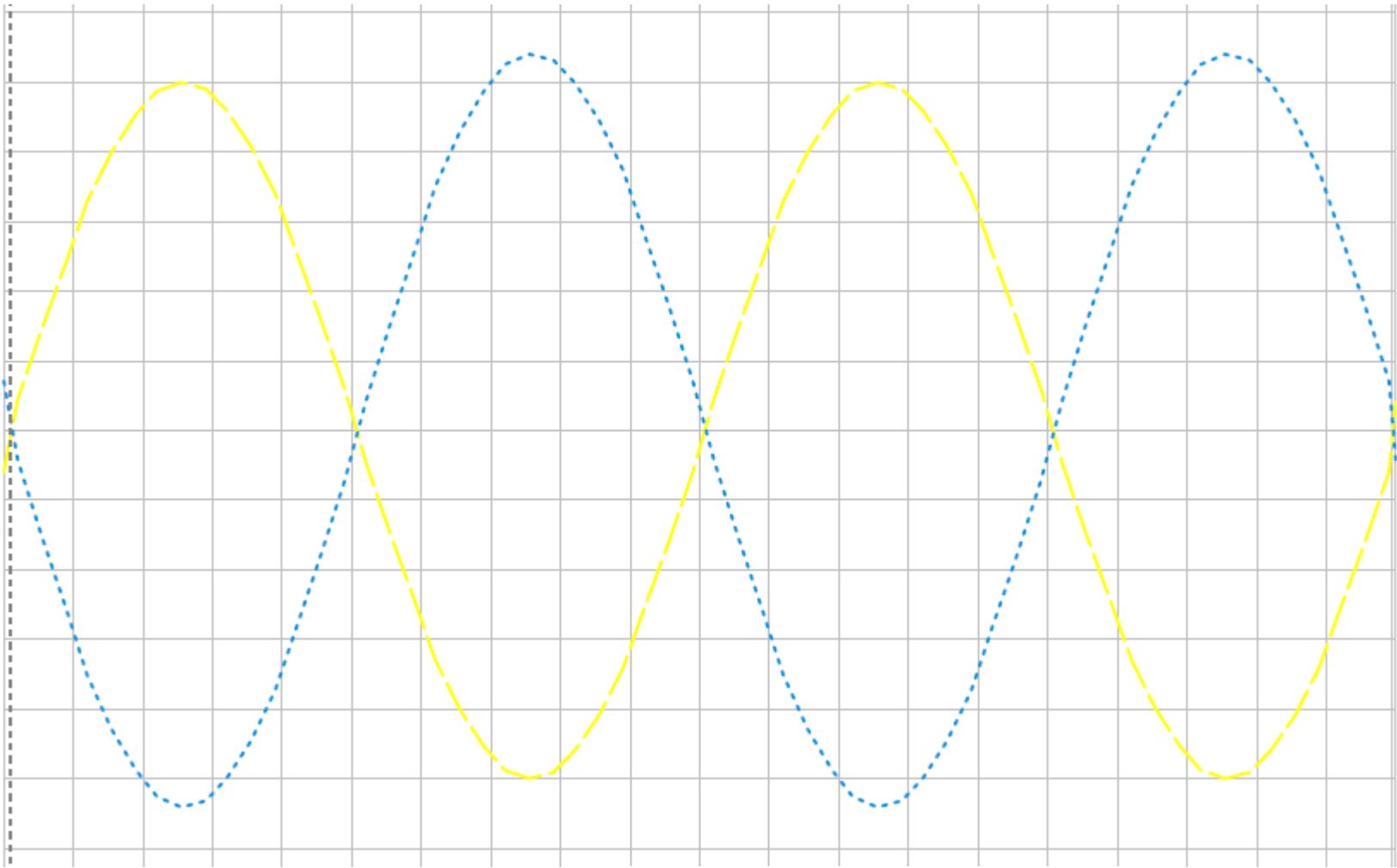
Inverting Amplifier circuit



$$V_{out} = -\frac{R_f}{R_{in}} V_{in}.$$

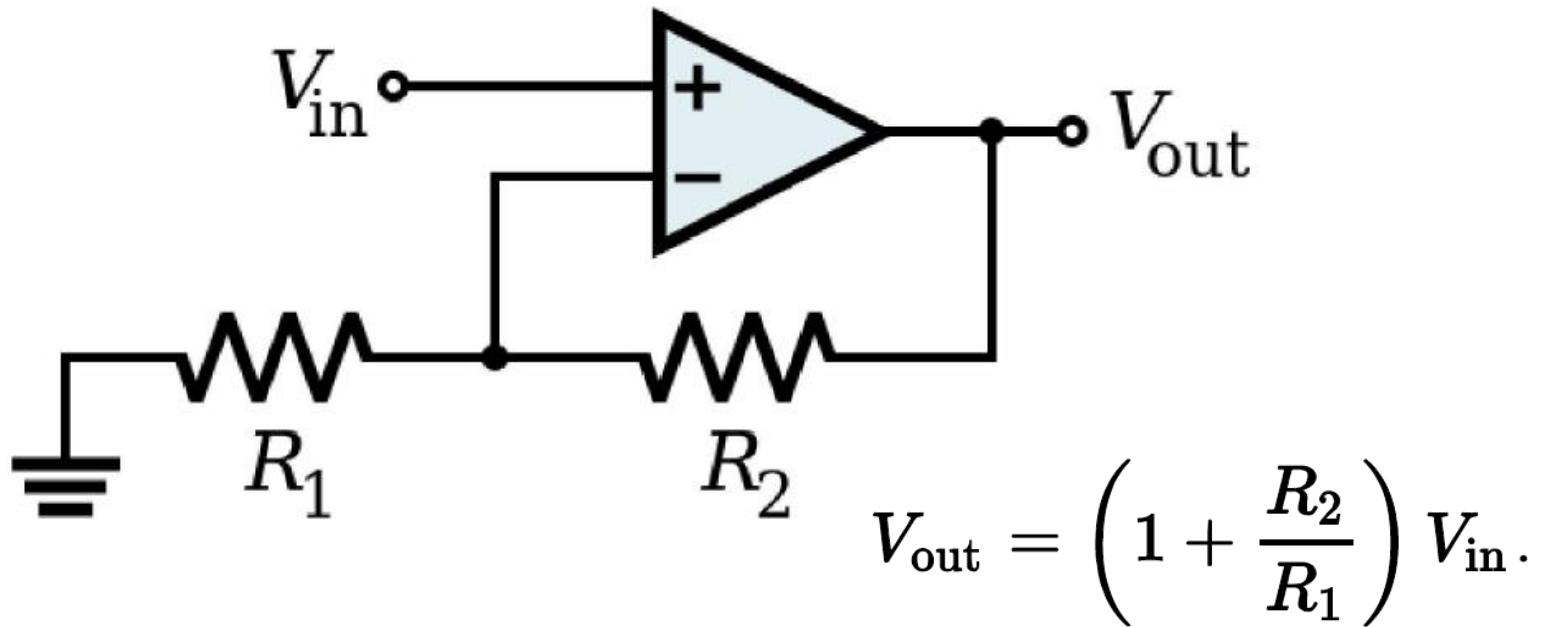
Inverting Amplifier simulate



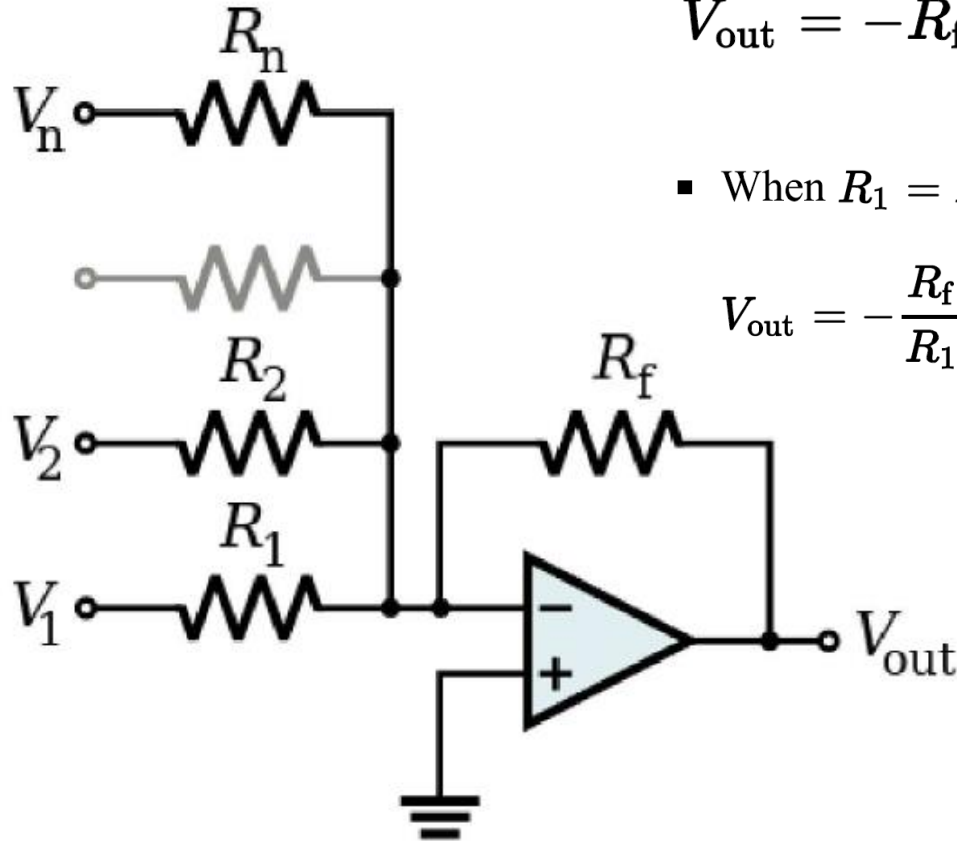


	Channel A	Channel B		
V/Div	10.00 mV	100.00 mV	Source	Trace
Offset	0.00 V	0.00 V	Position	10.00 uS
Invert	Normal	Normal	S/Div	100.00 uS
Coupling	AC	AC		

Non-inverting Amplifier circuit



Adder circuit or summing amplifier

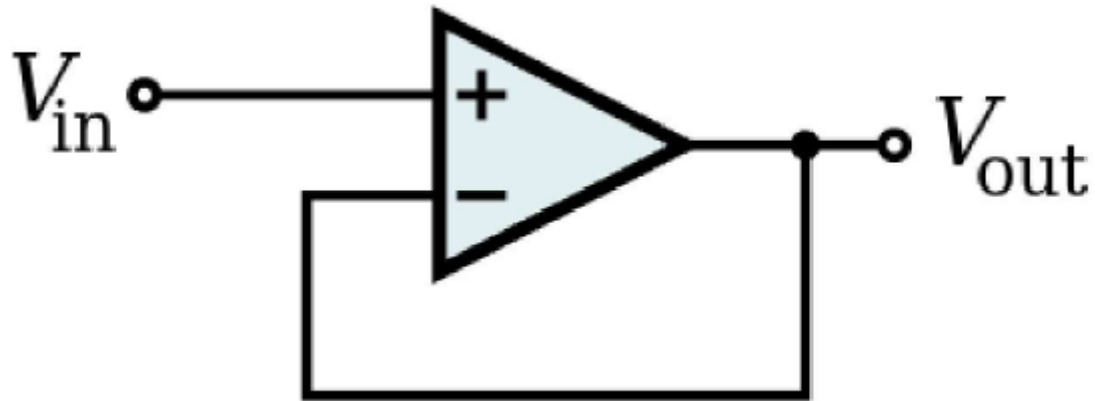


$$V_{out} = -R_f \left(\frac{V_1}{R_1} + \frac{V_2}{R_2} + \dots + \frac{V_n}{R_n} \right)$$

- When $R_1 = R_2 = \dots = R_n$, and R_f independent

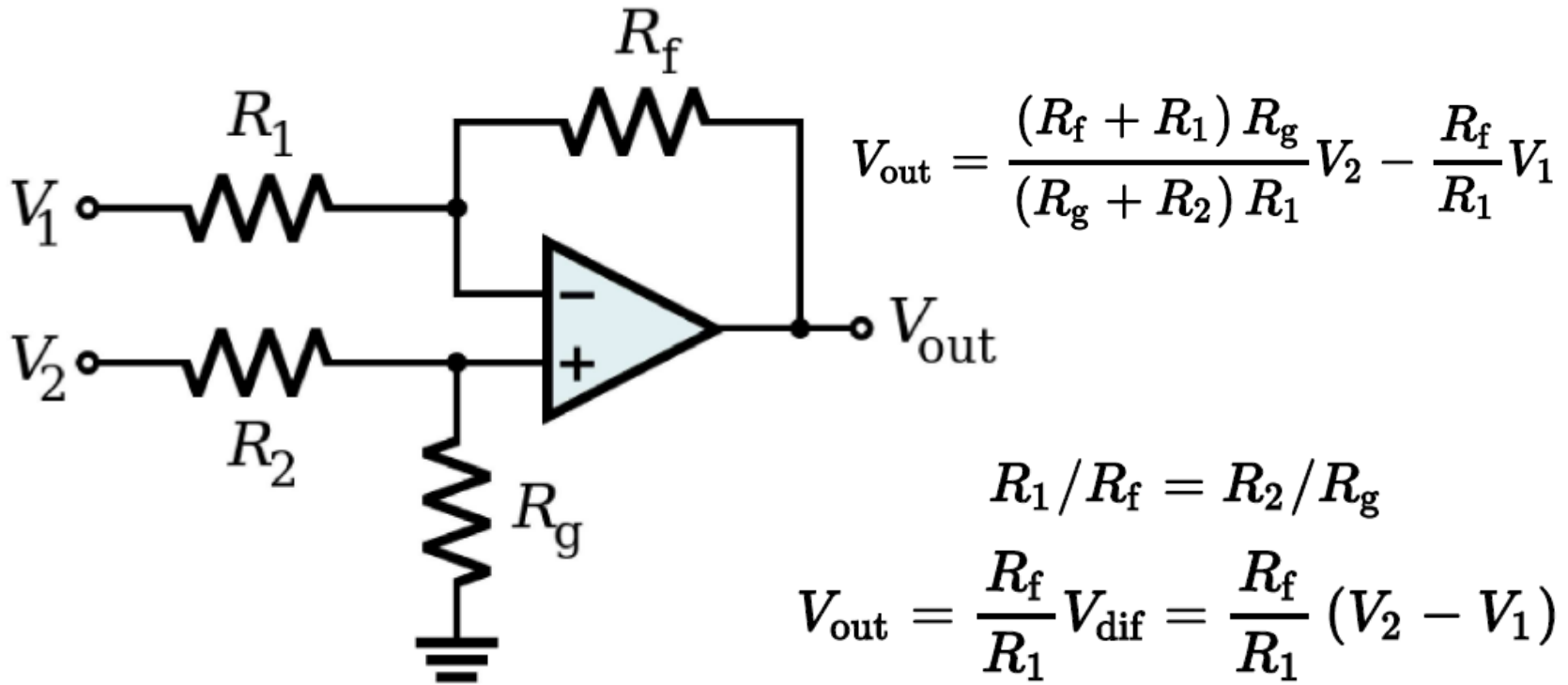
$$V_{out} = -\frac{R_f}{R_1} (V_1 + V_2 + \dots + V_n)$$

Voltage follower or buffer amplifier

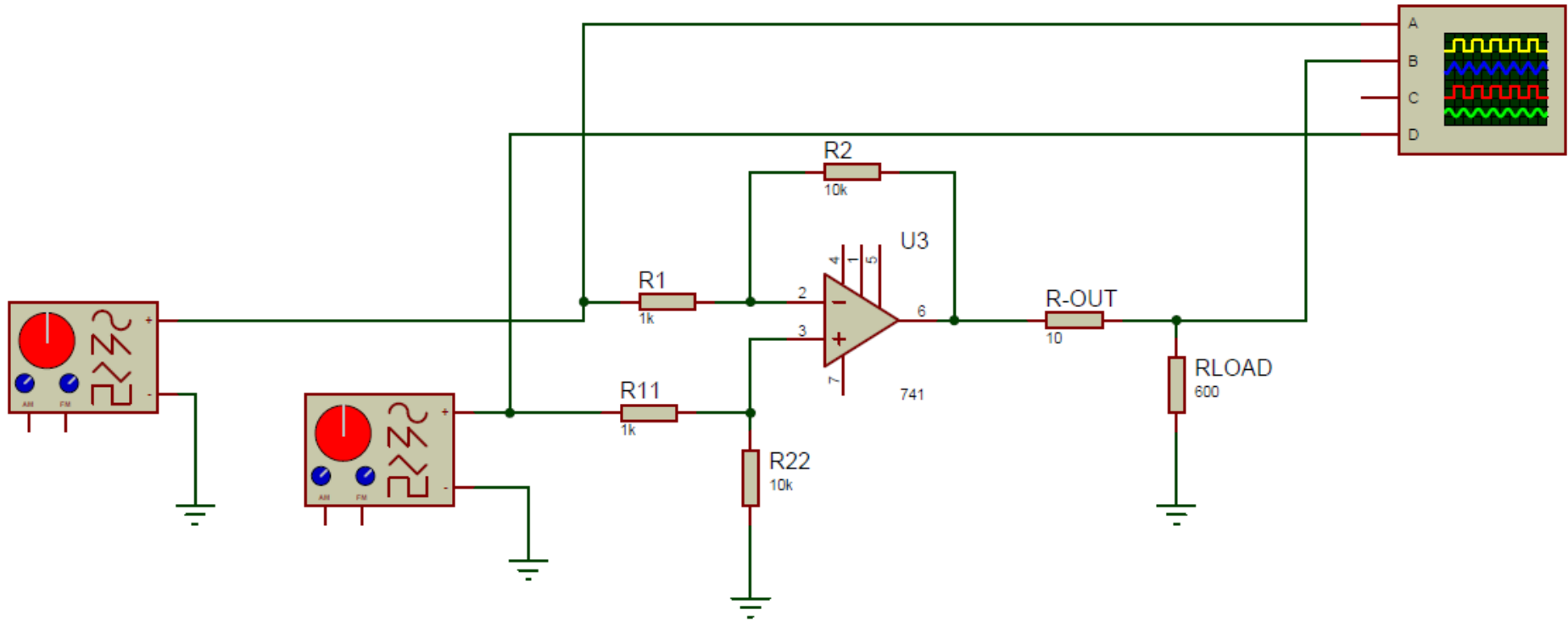


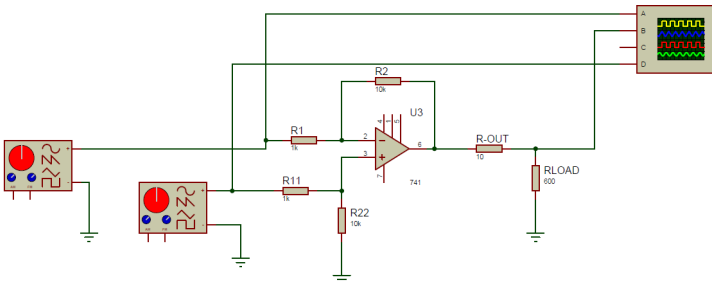
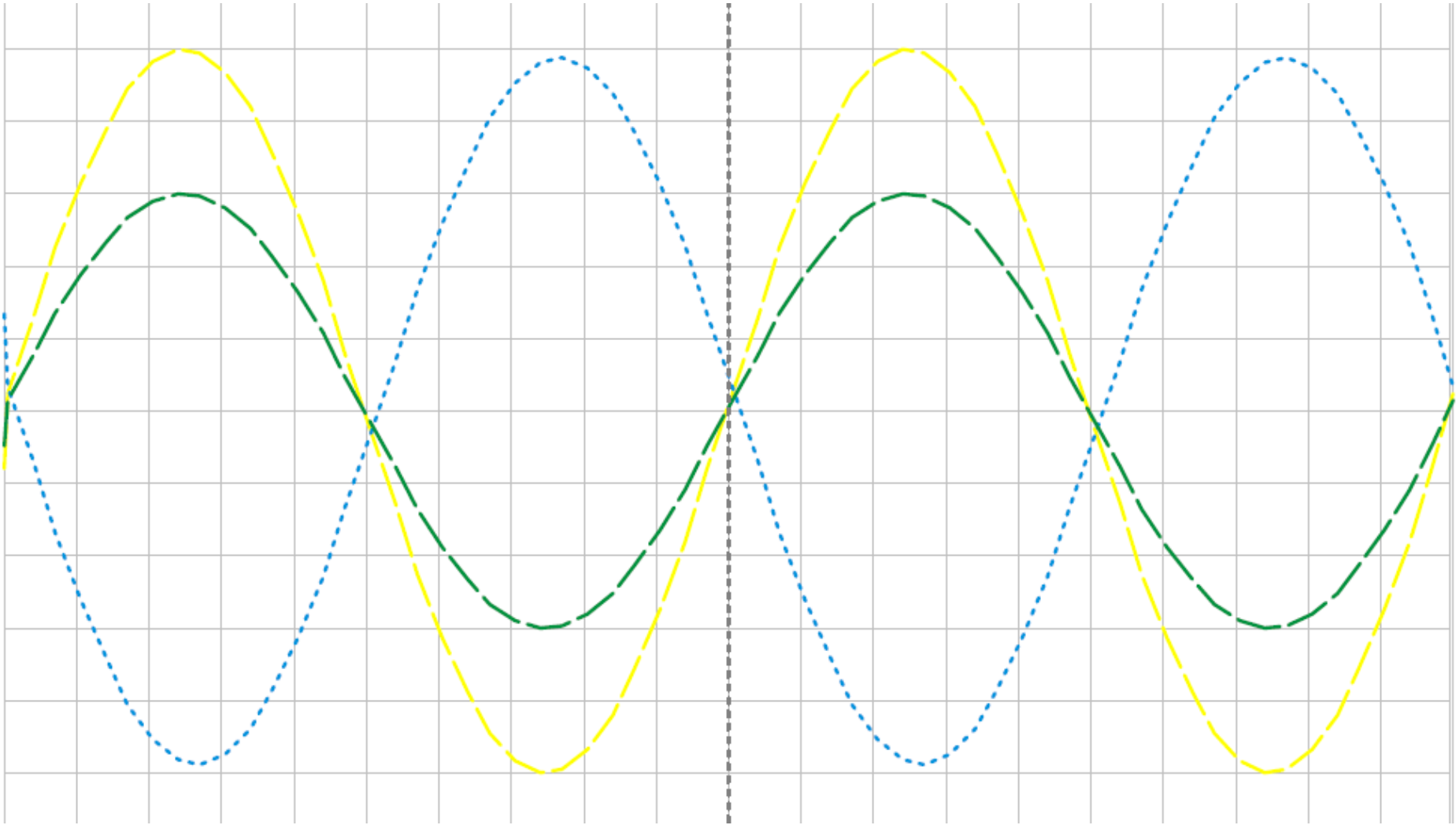
$$V_{out} = V_{in}$$

Sub-tractor circuit or Differential Amplifier



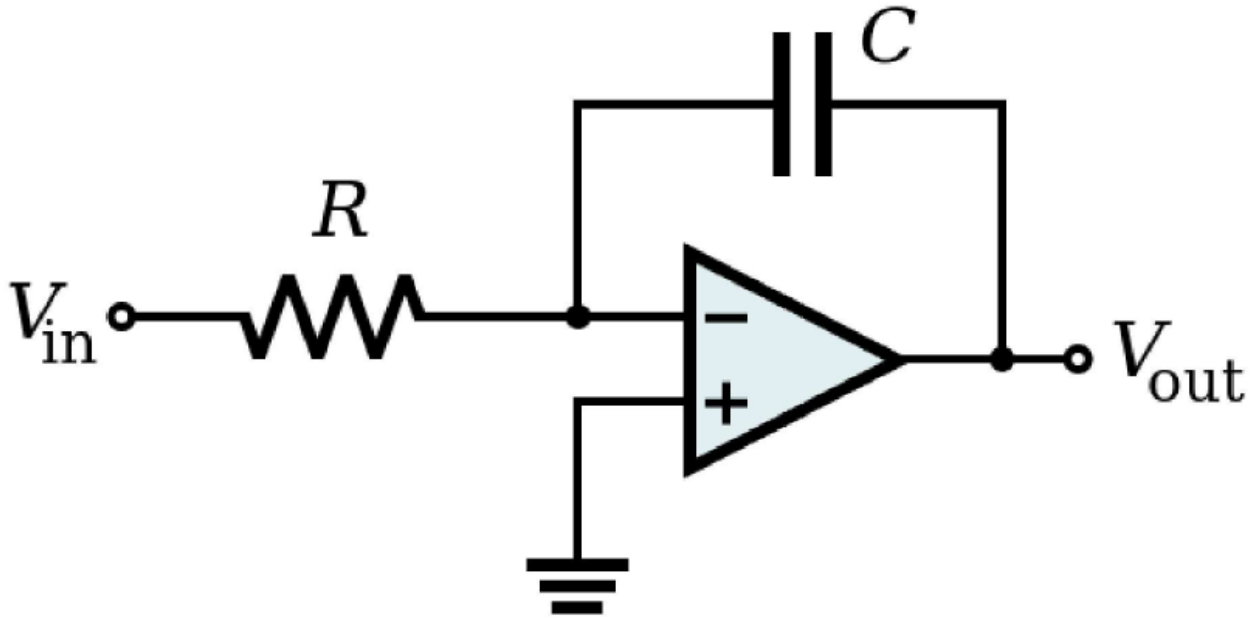
Sub-tractor circuit or Differential Amplifier simulate





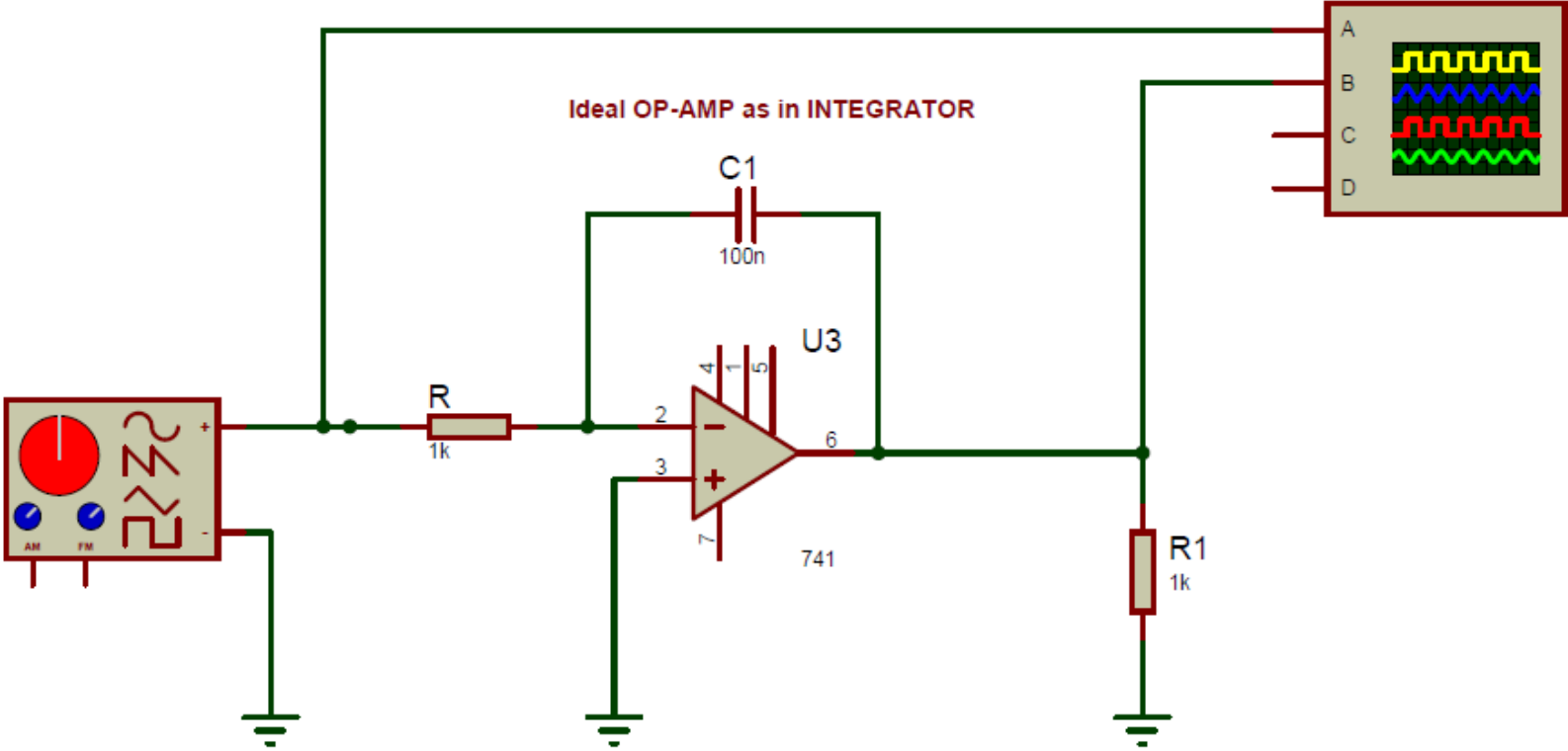
	Channel A	Channel B	Channel C	Channel D
V/Div	50.00 mV	200.00 mV	200.00 mV	50.00 mV
Offset	0.00 V	0.00 V	-800.00 mV	-20.00 mV
Invert	Normal	Normal	Normal	Normal
Coupling	AC	AC	Off	AC

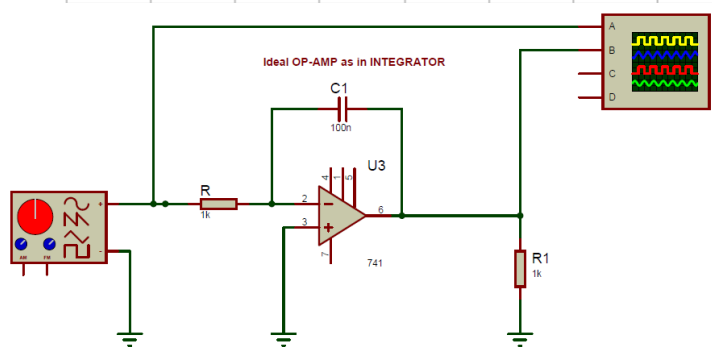
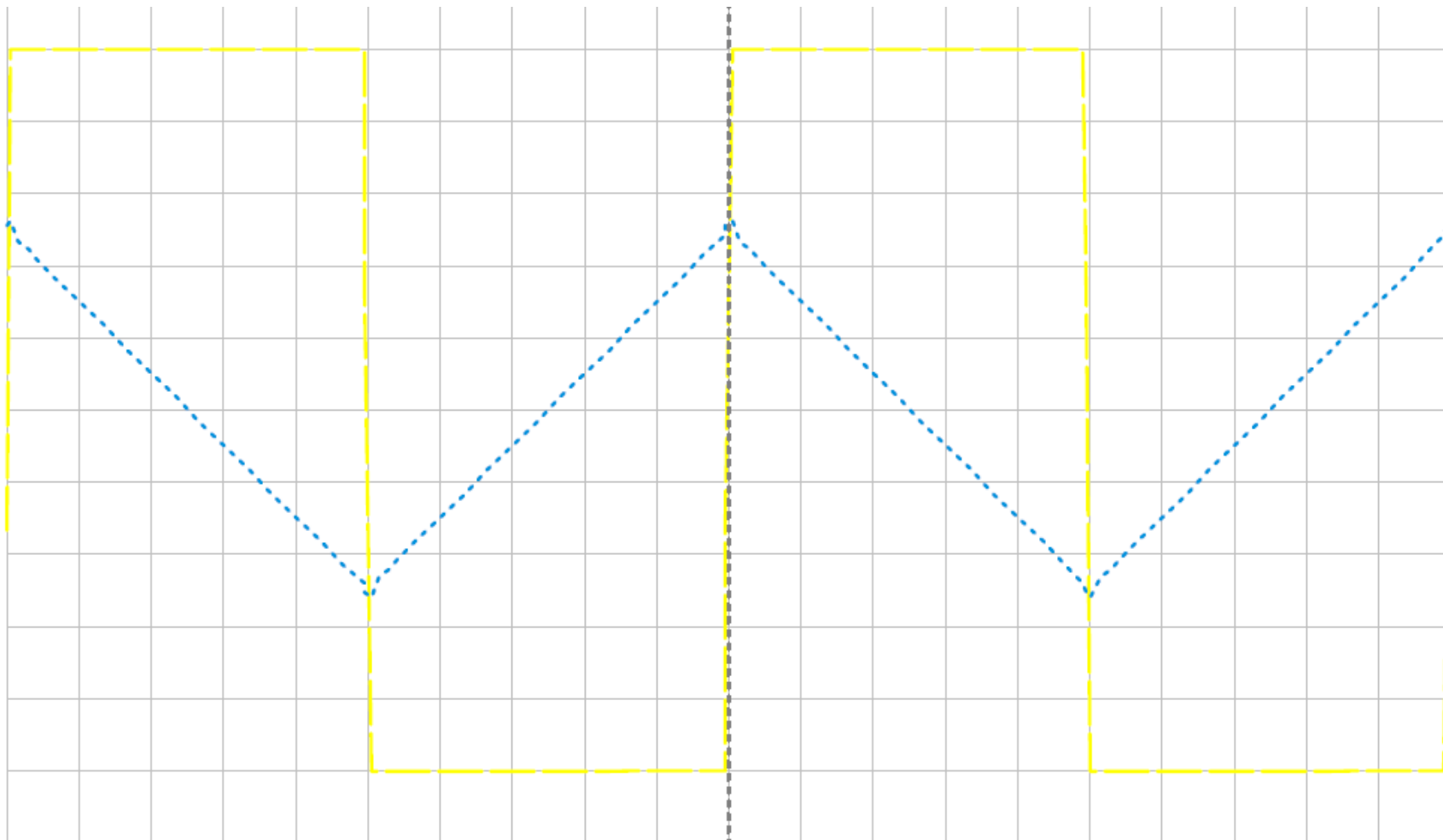
Inverting integrator circuit



$$V_{out}(t_1) = V_{out}(t_0) - \frac{1}{RC} \int_{t_0}^{t_1} V_{in}(t) dt$$

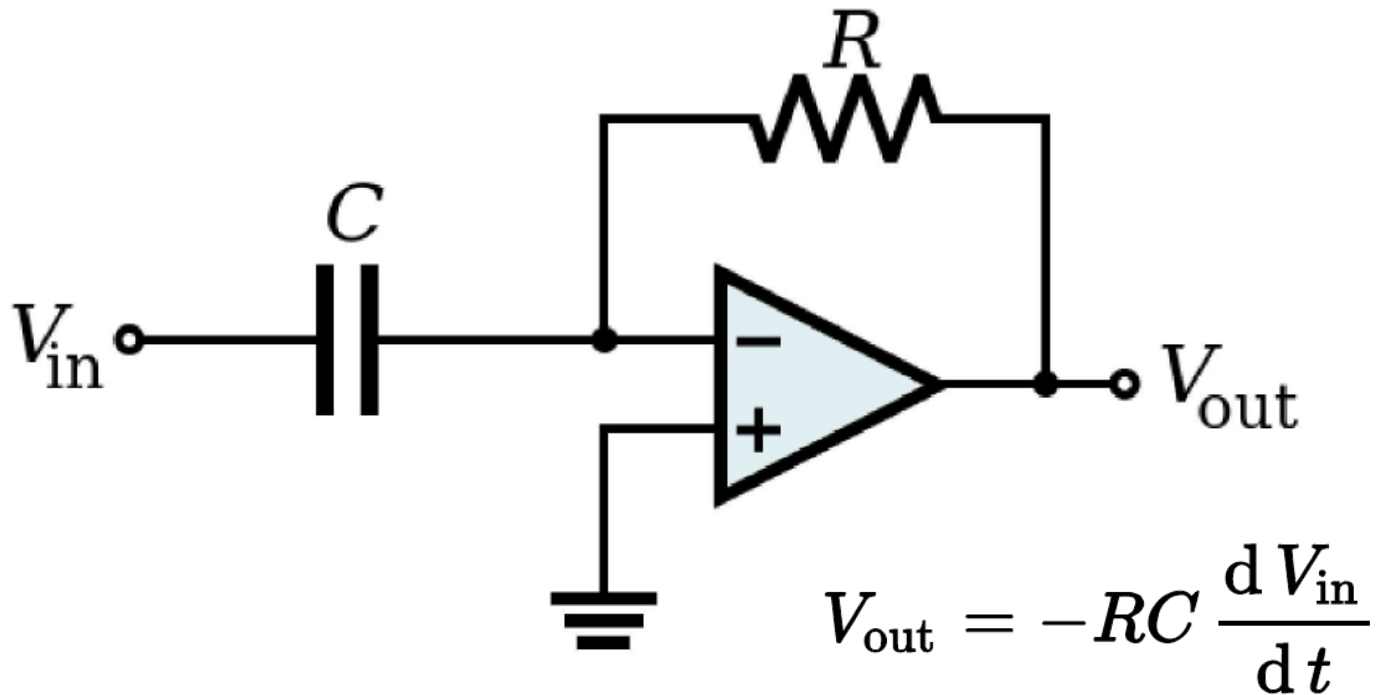
Inverting integrator simulate





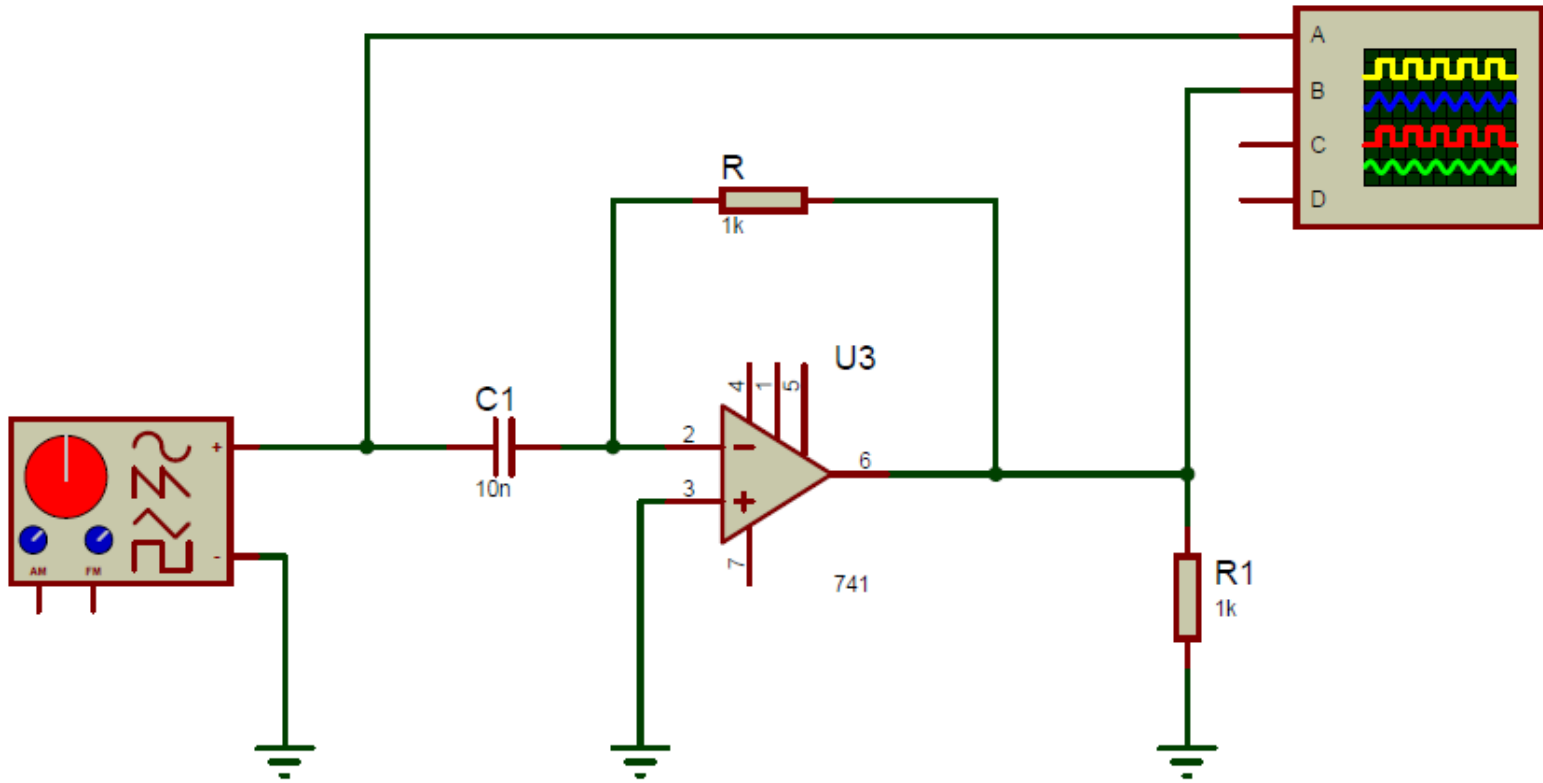
	Channel A	Channel B		
V/Div	10.00 mV	5.00 mV		Horizontal
Offset	0.00 V	0.00 V	Source	Trace
Invert	Normal	Normal	Position	100.00 μS
Coupling	AC	AC	S/Div	10.00 μS

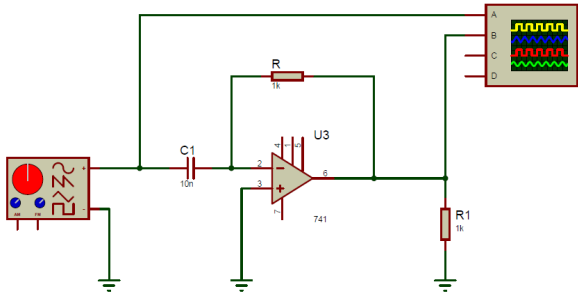
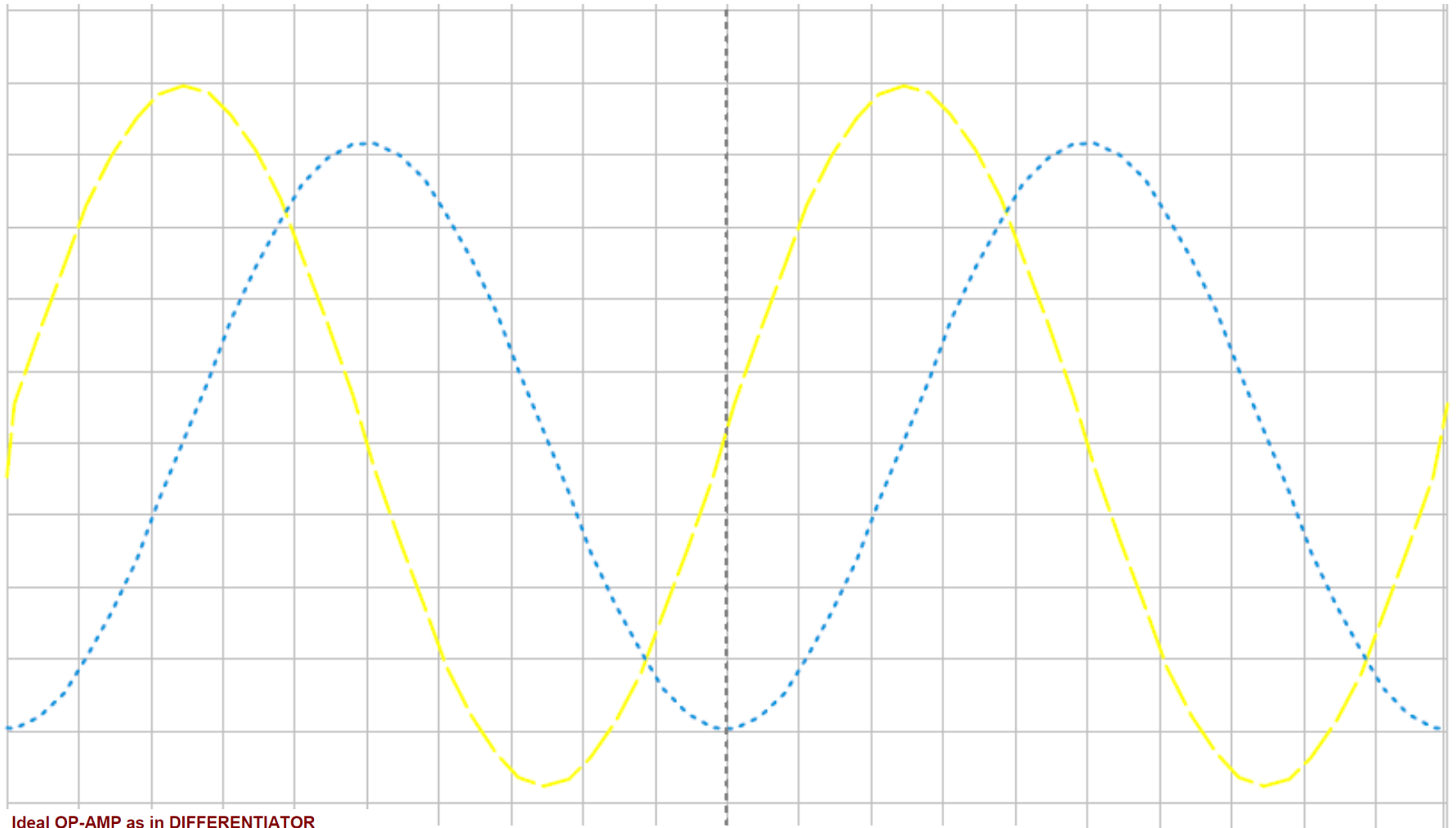
Inverting differentiator circuit



Inverting differentiator simulate

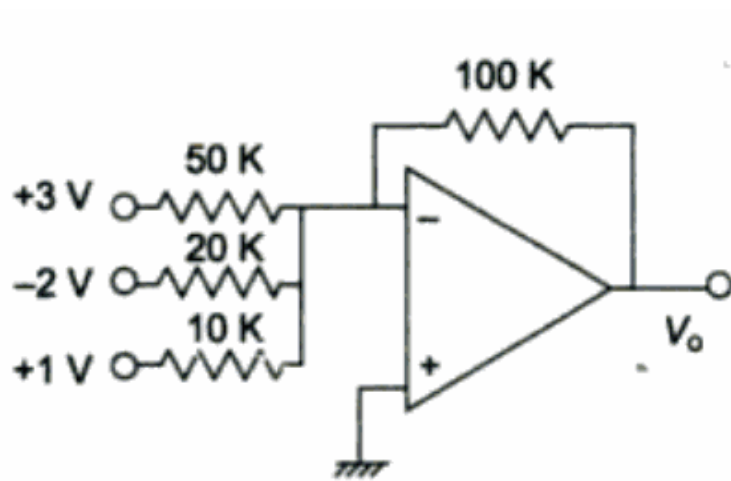
Ideal OP-AMP as in DIFFERENTIATOR



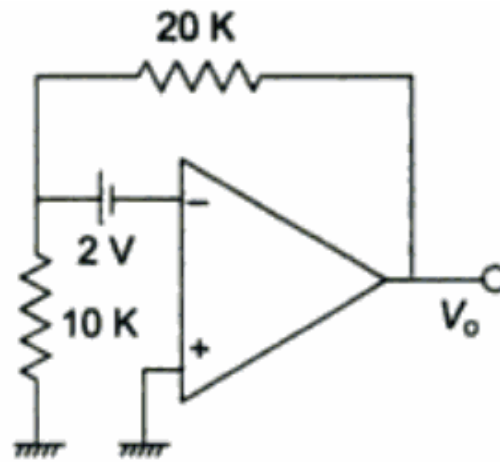


V/Div	65.83 mV	Channel A	Channel B	Horizontal
Offset	13.17 mV		5.00 mV	Trace
Invert	Normal		1.00 mV	Position
Coupling	AC		Normal	1.00 mS
			AC	S/Div
				100.00 uS

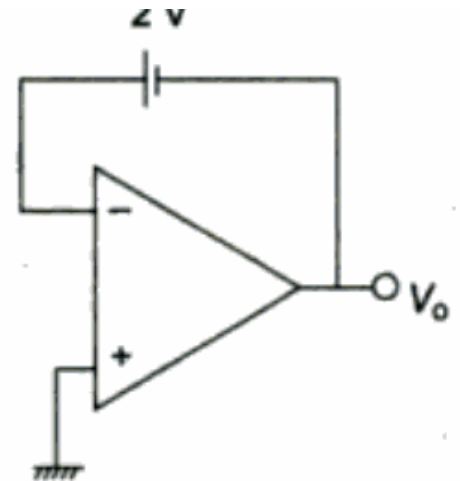
11. Assuming the operational amplifier to be ideal, calculate V_o for circuits a to g.



(a)

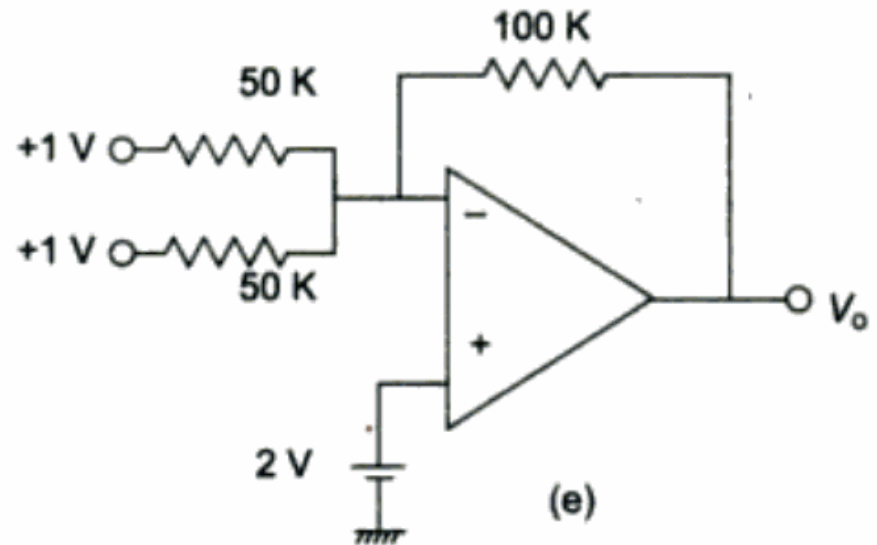
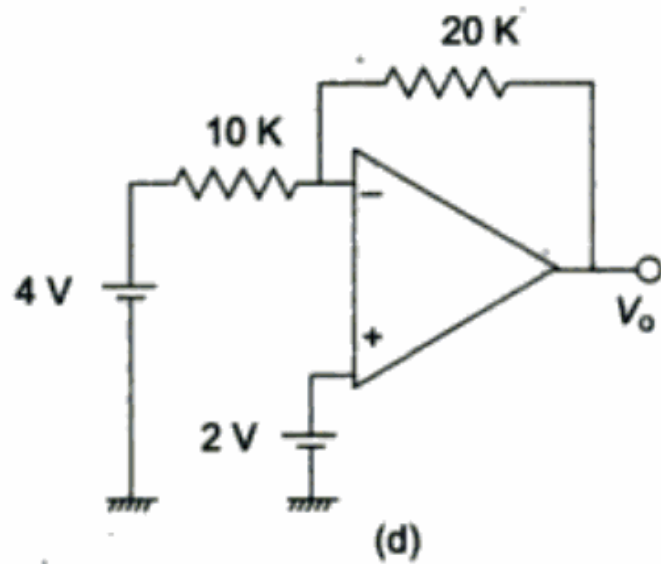


(b)

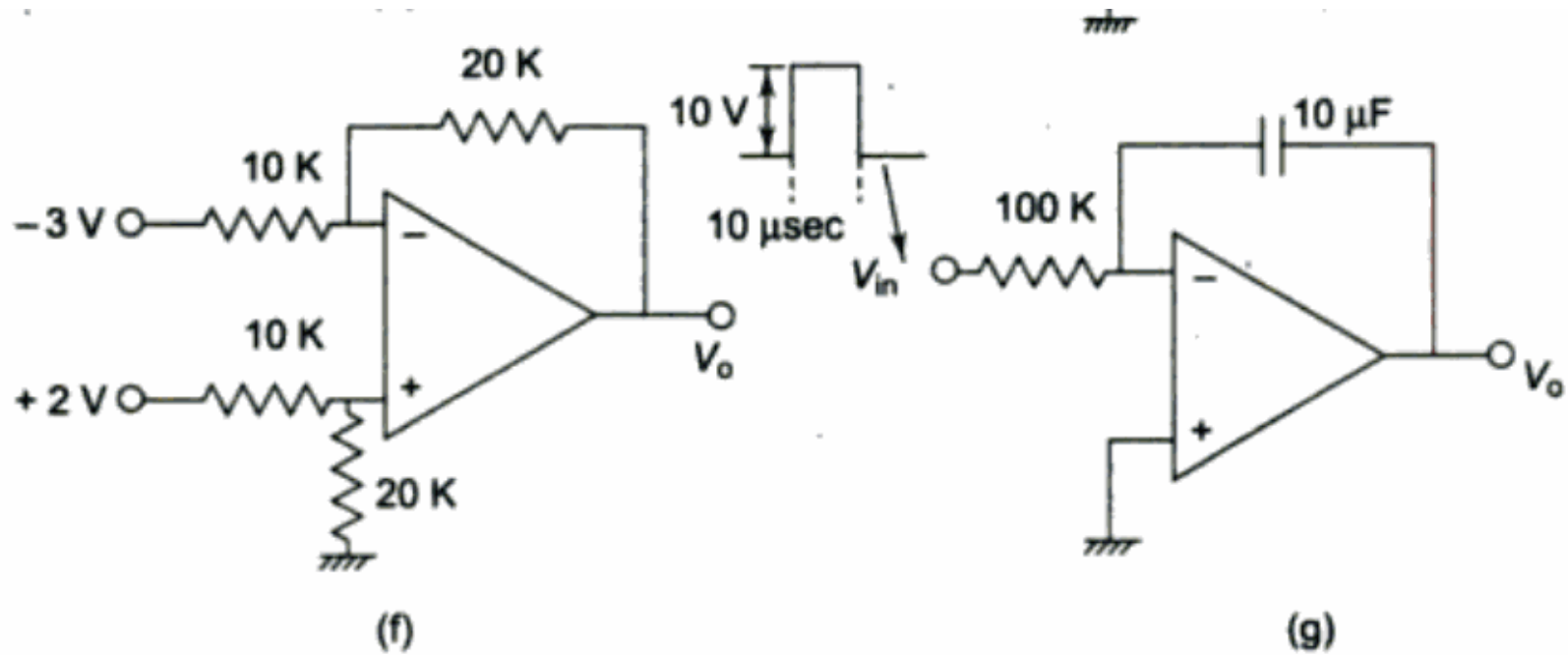


(c)

11. Assuming the operational amplifier to be ideal, calculate V_o for circuits a to g.



11. Assuming the operational amplifier to be ideal, calculate V_o for circuits a to g.



12. Assuming the operational amplifier to be ideal, find the values of circuit for (i), (ii) and (iii).
- (i) Amp voltage gain = -5 and input resistance 100 K
 - (ii) Amp voltage gain = -20 and input resistance 2 K
 - (iii) Voltage gain = $+100$ and high gain input resistance

